MOSFETs Silicon N-channel MOS (U-MOSIV)

TK80F04K3L

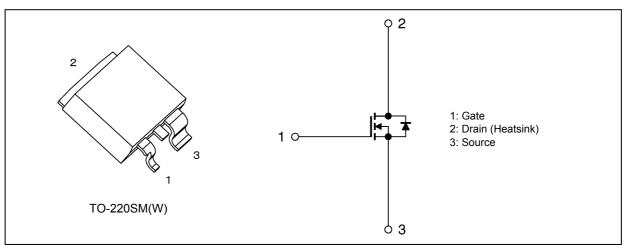
1. Applications

- Automotive
- Switching Voltage Regulators
- DC-DC Converters
- Motor Drivers

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 3.3 \text{ m}\Omega$ (typ.) ($V_{GS} = 10 \text{ V}$)
- (2) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 40 \ V)$
- (3) Enhancement mode: $V_{\rm th}$ = 2.0 to 3.0 V (V_{\rm DS} = 10 V, $I_{\rm D}$ = 1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics				Rating	Unit
Drain-source voltage			V _{DSS}	40	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)		(Note 1)	I _D	80	A
Drain current (pulsed)		(Note 1)	I _{DP}	240	
Power dissipation	(T _c = 25°C)		PD	125	w
Single-pulse avalanche energy		(Note 2)	E _{AS}	86	mJ
Avalanche current			I _{AR}	80	A
Channel temperature		(Note 3)	T _{ch}	175	°C
Storage temperature		(Note 3)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	1.2	°C/W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 14 μ H, R_G = 25 Ω , I_{AR} = 80 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

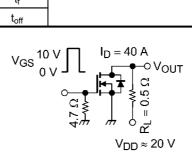
6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V			±1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	—	—	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	40	_	_	V
Drain-source breakdown voltage	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	20	_	_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	3.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 6 V, I _D = 40 A	_	4.5	6.7	mΩ
		V _{GS} = 10 V, I _D = 40 A	_	3.3	4.1	

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	—	3450	_	pF
Reverse transfer capacitance	C _{rss}]	_	450	_	
Output capacitance	C _{oss}		—	700	_	
Switching time (rise time)	tr	See Fig. 6.2.1	_	12	_	ns
Switching time (turn-on time)	t _{on}]	_	27	_	
Switching time (fall time)	t _f]	_	19	_	
Switching time (turn-off time)	t _{off}]	_	65	_	



Duty \leq 1%, t_W = 10 μs

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx 32 \text{ V}, \text{ V}_{GS} \text{ = } 10 \text{ V}, \text{ I}_{D} \text{ = } 80 \text{ A}$	_	75	—	nC
Gate-source charge	Q _{gs}		_	49	_	
Gate-drain charge	Q _{gd}			26	_	

6.4. Source-Drain Characteristics (Ta = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I _{DR}	—	_	_	80	А
Reverse drain current (pulsed)	(Note 4)	I _{DRP}	—	_	_	240	
Diode forward voltage		V _{DSF}	I _{DR} = 80 A, V _{GS} = 0 V	_	—	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 80 A, V _{GS} = 0 V	_	51	_	ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 50 A/μs	_	31	—	nC

Note 4: Ensure that the channel temperature does not exceed 175°C.

7. Marking (Note)

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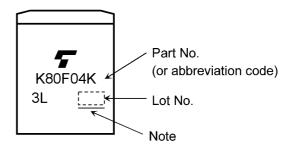


Fig. 7.1 Marking

 Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]] Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

8. Moisture-Proof Packing

This device is packed in a moisture-proof laminated aluminum bag.

8.1. Precautions for Transportation and Storage (Note)

- (1) Avoid excessive vibration during transportation.
- (2) Do not toss or drop the packed devices to avoid ripping of the bag.
- (3) After opening the moisture-proof bag, the devices should be assembled within two weeks in an environment of 5°C to 30°C and RH70% or below. Perform reflow at most twice.
- (4) The moisture-proof bag may be stored unopened for up to 24 months at 5°C to 30°C and RH90% or below.
- (5) If, upon opening the bag, the moisture indicator card shows humidity of 30% or above (the color of the 30% dot has changed from blue to pink) or the expiration date has passed, the devices should be baked as follows:

Baking conditions: 125°C for 48 hours.

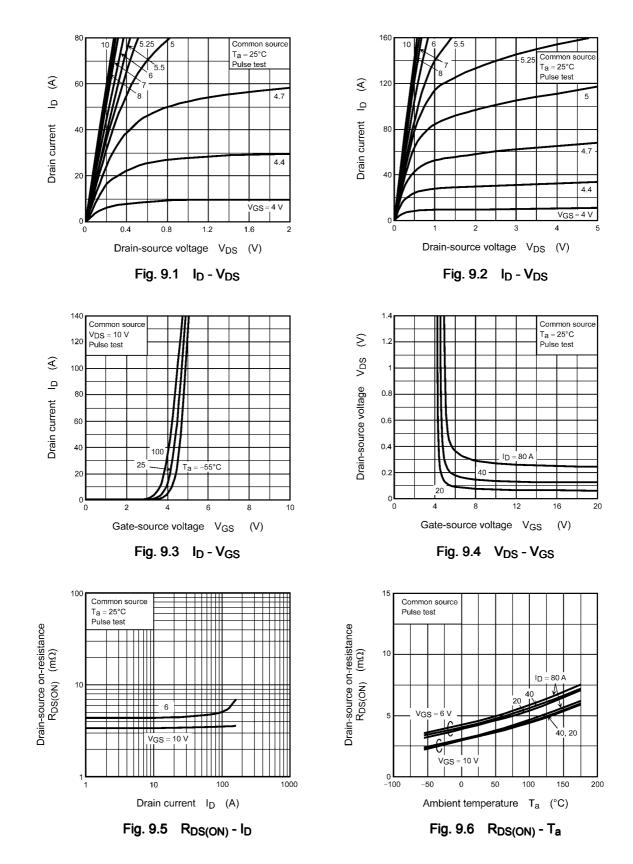
Note: Since the tape materials are not heat-proof, devices should be placed on either heat-proof trays or aluminum magazines when baking.



The humidity indicator shows an approximate ambient humidity at 25°C. If the ambient humidity is below 30%, the color of all the indicator dots is blue. If, upon opening the bag, the color of the 30% dot has changed from blue to pink, the devices should be baked before assembly.

Fig. 8.1.1 Humidity Indicator

9. Characteristics Curves (Note)



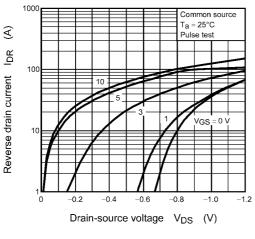


Fig. 9.7 I_{DR} - V_{DS}

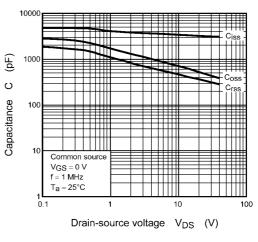


Fig. 9.8 Capacitance - VDS

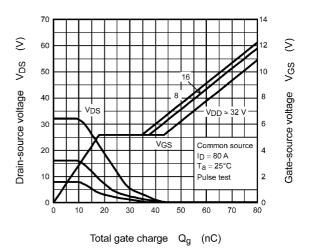


Fig. 9.10 Dynamic Input/Output Characteristics

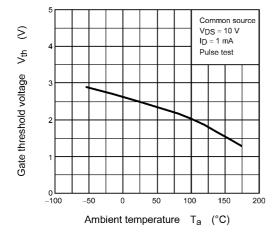
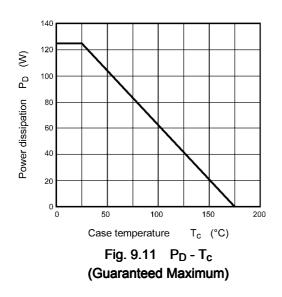


Fig. 9.9 V_{th} - T_a



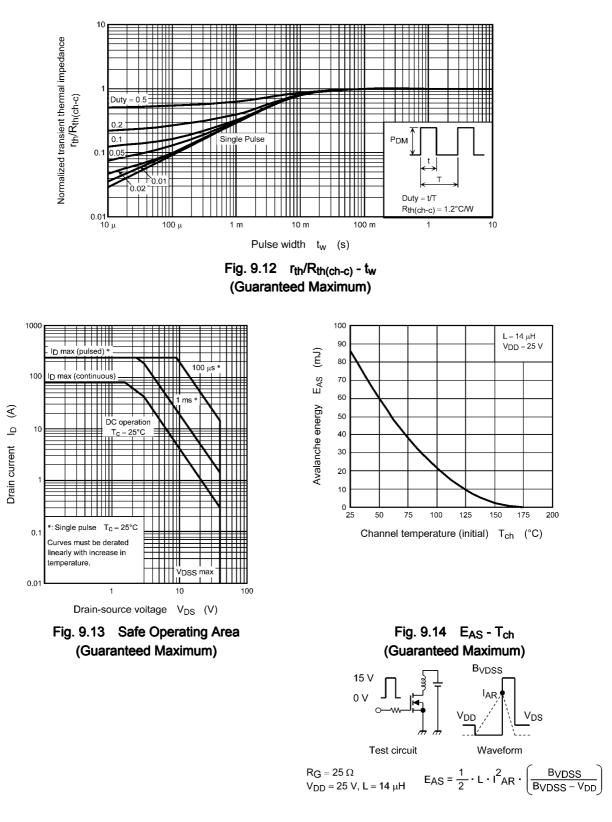


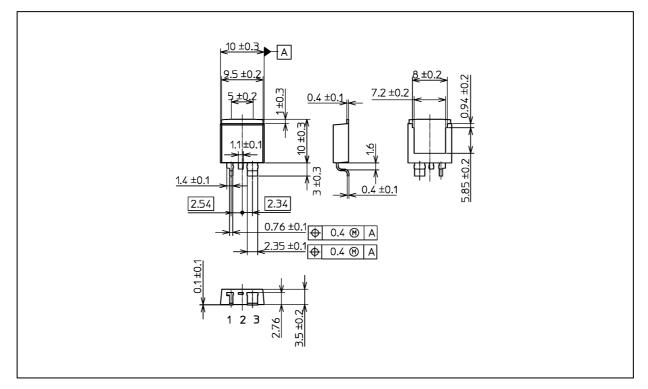
Fig. 9.15 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

TK80F04K3L

Package Dimensions

Unit: mm



Weight: 1.07 g (typ.)

Package Name(s)
TOSHIBA: 2-10W1S
Nickname: TO-220SM(W)

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